Corrigendum for Bid Specifications on GeM for Procurement of 30 Desktops

Reference: GeM Bid Number- GEM/2024/B/5488274

In response to queries and feedback received from bidders, a review of the desktop specifications for the above-mentioned bid has been conducted. The following updates to the specifications are hereby revised to better align with the requirements of business and enterprise applications:

Revised Specifications:

- 1. Processor Number(Point No. 9 in additional specification parameter):
 - Original Requirement: Specific model numbers of Intel processors were mentioned.
 - Revised Requirement: The condition requiring specific processor model numbers has been removed.
- 2. Chipset Series (Point No. 10 in additional specification parameter):
 - Original Requirement: Intel Z-series chipset.
 - Revised Requirement: Intel Q-series chipset or Intel H-series chipset

Action Required:

We are now issuing this corrigendum to reflect these updates. Bidders are encouraged to review these revised specifications and adjust their proposals accordingly.

Approval and Issuance:

This corrigendum is being issued following the approval from the competent authority.

Approved

